Understanding process-dependent oxygen vacancies in thin $\text{HfO}_2/\text{SiO}_2$ stacked-films on Si (100) via competing electron-hole injection dynamic contributions to second harmonic generation.

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Optical Second Harmonic Generation (SHG) is used to characterize charge trapping dynamics in thin HfO$_2$ films deposited on chemically oxidized P-type Si(100) substrates. Previous work identified Electrostatic Field Induced Second Harmonic (EFISH) generation as the dominant process-dependent contribution to the nonlinear response of such structures. EFISH generation was attributed to charges trapped primarily in oxygen vacancy defects located at the SiO$_2$/HfO$_2$ interface that create an electrostatic field acting on the silicon space charge region. Here, we extend the understanding of charge trapping by monitoring SHG as HfO$_2$ thickness and post-deposition anneal (PDA) temperature are modified. Corresponding trends in time-dependent SHG are identified that reflect unique contributions from competing electron and hole injection dynamics. Specifically, we attribute the initial increase in Time Dependent (TD) SHG intensity to photo-excited electrons from the silicon dominating the electrostatic field response. The subsequent decrease in SHG intensity corresponds to the resonant tunneling of hole carriers trapped at the interface and creating a larger field but biased in the reverse direction of the initial dipole field located at the interface. These results show that SHG can provide an in situ, non-destructive diagnostic of charge trapping in thin gate dielectric films.

**Key take-home points:**

- **TD SHG governed by both electron and hole injection dynamics:**
  - Photo-excited electrons cause increase in SHG
  - Trap-assisted hole tunneling causes decrease in SHG
- **Density of charge trapping defects in the gate dielectric will promote charge transfer and build up of interfacial electric field**
- **SHG provides the ability to qualitatively understand gate oxide and interface integrity due to charge trapping defects**
High-\( k \) dielectric defects and device performance:

- **What are they? Anything that can trap charges.**
  - \( \text{O}_2 \), \( \text{N}_2 \), vacancies and / or interstitials.
  - Impurities (C, B, etc.).
  - Crystal imperfections (grain boundaries, surface states).
  - All are discrete localized states within the band gap.

- **How does this affect device performance?**
  - Degradation of carrier mobility.
  - Charge trapping.
  - Increase in leakage current.

\[ \text{Voltage \[-V\]} \]
\[ \begin{array}{c|c|c|c|c|c|c|c} \hline \text{Current Density \[-A/cm^2\]} & \text{0.0} & \text{0.5} & \text{1.0} & \text{1.5} & \text{2.0} & \text{2.5} \\ \hline \text{2.0nm HfO}_2 & \text{10} & \text{10} & \text{10} & \text{10} & \text{10} & \text{10} \\ \text{3.0nm HfO}_2 & \text{10} & \text{10} & \text{10} & \text{10} & \text{10} & \text{10} \\ \text{4.0nm HfO}_2 & \text{10} & \text{10} & \text{10} & \text{10} & \text{10} & \text{10} \\ \end{array} \]

\[ \text{Electric Field (MV/cm)} \]
\[ \begin{array}{c|c|c|c|c|c|c|c} \hline \text{Electric Field (MV/cm)} & \text{0.0} & \text{0.5} & \text{1.0} & \text{1.5} & \text{2.0} & \text{2.5} \\ \hline \text{SiO}_2 & \text{350} & \text{300} & \text{250} & \text{200} & \text{150} & \text{100} \\ \text{2nm} & \text{350} & \text{300} & \text{250} & \text{200} & \text{150} & \text{100} \\ \text{3nm} & \text{350} & \text{300} & \text{250} & \text{200} & \text{150} & \text{100} \\ \text{4nm} & \text{350} & \text{300} & \text{250} & \text{200} & \text{150} & \text{100} \\ \end{array} \]

Previous results:

\[ I_{PP}^{(2\omega)} = \left| a_0^{PP} + a_4^{PP} \cos(4\phi) \right|^2 \]

- \( a_4^{PP} \) is from silicon, but very weak

\[ a_0^{PP} = a_0^{Si/SiO_2} + a_0^{SiO_2/High-k} + a_{EFISH}^{Si} \]

Previous work demonstrated these two coefficients are negligible.

- \( a_{EFISH}^{Si} \neq 0 \) (Dominant contribution)

Electric Field Induced Second Harmonic Generation (EFISH)

Varying Composition:

\[
\text{Si/ SiO}_2/ \text{Hf}_{(1-x)}\text{Si}_x\text{O}_2
\]

What did we learn?

- Interfacial electrostatic fields are primary contribution to SHG response.
- These interfacial fields are modified by subtle film growth conditions (composition, anneal temperature, and thickness).
- Photo-excited charge carriers in the substrate, trapped in the dielectric defect centers, are the source of these DC field.

*Fig. 3. p-in/p-out RA-SHG from samples with varying Si content \(x\). (a) As-deposited samples. (b) Samples annealed at 700 °C for 60 s in 30 Torr ammonia. The azimuthal range has been reduced for brevity.*

*R. Carriles, et. al., JVST B, 24, 2160 (2006)*
SHG measurements:

• Second Harmonic Generation: \( P_i^{(2\omega)} = \chi^{(2)}_{ijk} E_j^{(\omega)} E_k^{(\omega)} \)

• Symmetry arguments govern SHG: \( \chi^{(2),\text{bulk}}_{ijk} \neq 0 \), \( \chi^{(2),\text{surface}}_{ijk} \neq 0 \), \( \chi^{(2),\text{EFISH}}_{ijk} \neq 0 \)

• TD-SHG: \( I^{2\omega}(t) \propto |\chi^{(2)} + \chi^{(3)} E_{DC}(t)|^2 I^2(\omega) \)

Symmetry arguments govern SHG:  \( \chi^{(2),\text{bulk}}_{ijk} = 0 \),  \( \chi^{(2),\text{surface}}_{ijk} \neq 0 \),  \( \chi^{(2),\text{EFISH}}_{ijk} \neq 0 \)
Samples:

- HfO$_2$ deposited via Atomic Layer Deposition (ALD).
- Interfacial oxide thickness previously determined using HR-TEM.
- HfO$_2$ thickness verified using spectroscopic ellipsometry.
- Post Deposition and Rapid Thermal Anneals (PDA & RTA) performed ex-situ.

Objective: Investigate the evolution of HfO$_2$ crystallinity, bottom interfacial layer stoichiometry, and charge trap defect density as a function of thickness and anneal.

<table>
<thead>
<tr>
<th>Substrate</th>
<th>Interface</th>
<th>High-k</th>
<th>Anneal</th>
</tr>
</thead>
<tbody>
<tr>
<td>Si (100)</td>
<td>~1nm SiO$_2$</td>
<td>2nm HfO$_2$</td>
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</table>
Anatomy of a time dependent SHG measurement:

3 possible charge transfer mechanisms:

1. Photo-excited electron injection:
   - Si / SiO₂ band offsets = 4.3 eV. Therefore, 3 photon (hv = 1.6 eV) process necessary to inject electrons.

2. Photo-excited hole injection:
   - Si / SiO₂ band offsets = 5.8 eV. Therefore, 4 photon (hv = 1.6 eV) process necessary to inject holes.

3. Trap-assisted resonant tunneling:
   - Electrons or holes can tunnel through ~ 1nm interfacial layer.

E-field breaks translational symmetry and enhances SHG response

$t = 0$

- 750 nm
- Oxide
- Si substrate

$t > 0$

- 375 nm
- $2\omega$
- E-field breaks translational symmetry
- Enhances SHG response

$t >> 0$

- 375 nm
- $2\omega$
- E-field breaks translational symmetry
- Enhances SHG response

Diagrams showing charge injection and SHG response at different time intervals.
**Time Dependent SHG: As-deposited HfO$_2$**

**SHG time evolution:**
- **$t < 15$ sec:** SHG increases due to photo-excited electrons creating a stronger interfacial electrostatic field.
- **$t \sim 15$ sec:** SHG reaches a maxima due to competing electron/hole created fields equally opposing each other.
- **$t > 15$ sec:** SHG decreases due to resonant tunneling hole carriers dominating the build up of interfacial fields.
- **$t = 210-440$ sec:** dark period, system recovery.
- **$t > 440$ sec:** 3nm and 4nm HfO$_2$ SHG signals still dominated by resonant hole tunneling dynamics. 2nm HfO$_2$ SHG signal increases due to photo-excited electron injection.

**Initial built-in dipole field value**

Thicker samples have a stronger interfacial field causing decrease in SHG due to more bulk defects in the HfO$_2$ promoting trap assisted hole tunneling.

**$\lambda = 750$ nm**

**power = 250mW**
Time Dependent SHG: annealed HfO$_2$

SHG time evolution:

- $t < 160 \text{sec}$: SHG increases to a saturated intensity value due to photo-excited electrons creating a stronger interfacial electrostatic field.
- $t = 160-225 \text{sec}$: dark period, system recovery.
- $t > 225 \text{sec}$: 3nm and 4nm HfO$_2$ SHG signals monotonically rise to a saturated intensity. 2nm HfO$_2$ SHG signal unexpectedly decreases to the same saturated intensity value.

No decrease in SHG is observed for the annealed samples, corresponding to reversal of the interfacial electrostatic field, presumably due to less bulk defects available to promote trap assisted hole tunneling. Therefore, dominant charge transfer mechanism is photo-excited electron transfer to the surface.
Rotational Anisotropic SHG:

- Previous studies indicated dipolar coefficient’s ($a_0$) main contribution from EFISH.
- Except for the 4nm 700C sample, $a_0$ trends inversely with temperature and thickness.
- RA-SHG results indicate that EFISH contribution from charge trap centers is sensitive to both changes in temperature and film thickness.
Supporting optical measurements:

**Sub-\(E_g\) absorption (SE):**
- Charge trapping centers for a dielectric lie within the bandgap.
- Measure sub-bandgap absorption with spectroscopic ellipsometry.
- Sub-bandgap absorption “density” decreases with HfO\(_2\) film thickness, suggesting less charge trapping centers.
- Must ignore Si critical point absorption sites @ 3.4eV, 4.25eV, and 5.2eV (Price et. al., Appl. Phys. Lett. 2007)

**Grazing angle attenuated FTIR:**
- Absorption stretch at \(~1250\)cm\(^{-1}\) due to Si-O LO phonon mode.
- Since these films are HfO\(_2\) this absorption peak must originate from bottom oxide interfacial layer.
- Absorption strength increases with decreasing HfO\(_2\) thickness suggesting more stoichiometric bottom interfacial layer.
Conclusions:

- SHG demonstrates strong sensitivity towards process dependent changes in HfO$_2$ parameters such as thickness and anneal temperature.

- Observations of both electron and hole injection dynamics are observed for time dependent measurements.

- Trap-assisted hole tunneling dominates the TD-SHG response for as-deposited HfO$_2$ samples presumably due to a larger density of trapped charge centers in the dielectric promoting hole transfer.

- A qualitative understanding of the relative charge trap density and overall integrity of the dielectric film may be inferred from analysis of time dependent SHG dynamics.

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